

DSG-NPS R&D Meeting Minutes

Date: April 12, 2022

Time: 02:00PM – 02:30PM

Attendees: Mary Ann Antonioli, Peter Bonneau, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Mindy Leffel, Tyler Lemon, and Marc McMullen

1. Hardware Interlock System Development

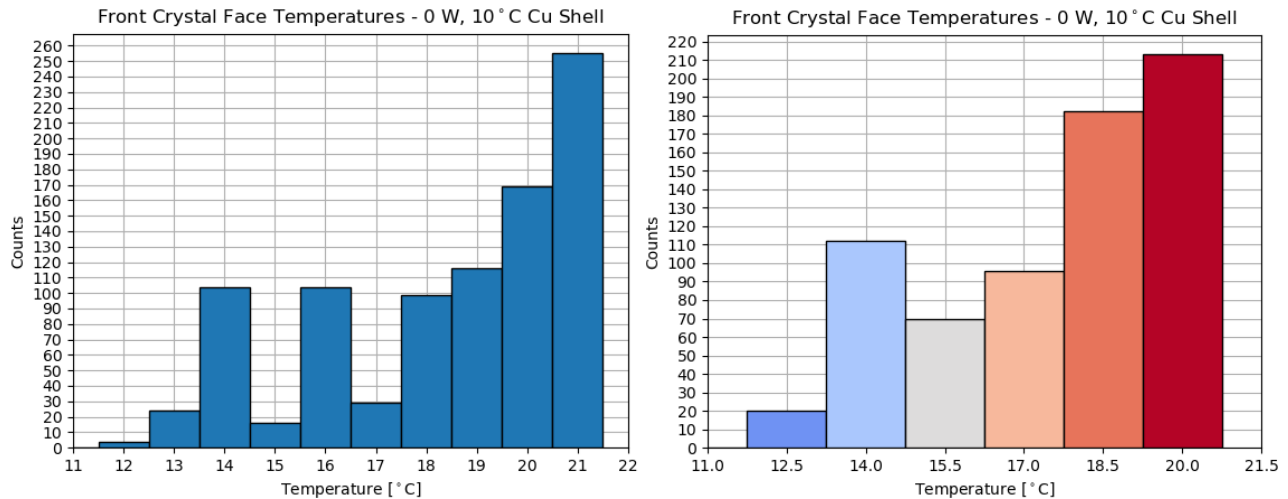
Mary Ann Antonioli, Peter Bonneau, Aaron Brown, and Tyler Lemon

1. Reviewed CAEN high voltage Phoebus screens
 - Action buttons should be labeled in a way that clearly communicates what the button does
2. High voltage supply cable Radial connector potting – six of 40 complete

2. Ansys thermal analysis

Aaron Brown, Pablo Campero, and Brian Eng

1. Reviewed histogram and streamline plots generated for front crystal face temperature data exported from Ansys crystal zone thermal simulation
 - Histogram with reduced bin widths generated to investigate question of accuracy of 14°C counts
 - Generated streamline plot shows heat flow converging in center of crystal array



Histograms of front crystal face temperatures with bin widths of 1°C (left) and 1.5°C (right)

